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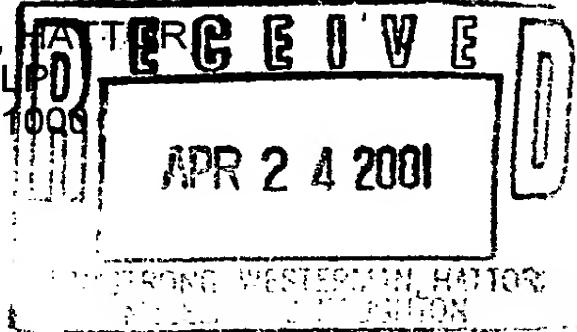


UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/766,656	01/23/2001	Norio Fukasawa	980233B

23850
ARMSTRONG, WESTERMAN, HATTER,
MCLELAND & NAUGHTON, LLP
1725 K STREET, NW, SUITE 1000
WASHINGTON, DC 20006



CONFIRMATION NO. 1144

FORMALITIES LETTER



OC00000005992435

Date Mailed: 04/20/2001

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

- A substitute specification in compliance with 37 CFR 1.52 because:
 - Papers contain improper margins. *Each sheet must have a left margin of at least 2.5 cm (1") and top, bottom and right margins of at least 2.0 cm (3/4")*
- Substitute drawings in compliance with 37 CFR 1.84 because:
 - drawing figures contain text that is not in English (including, for example, a flow chart that was originally not in English that has been marked up to include the English text) see 37 CFR 1.84(p)(2) and 37 CFR 1.52(d)(1);

*A copy of this notice **MUST** be returned with the reply.*

2/26/01

Customer Service Center
Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

030

In re the Application of: **Norio FUKASAWA et al.**

Serial No.: **09/766,656**

JFC

Filed: **January 23, 2001**

Attn: Applications Division

For: **METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR DEVICE,
SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING THE DEVICE**

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Commissioner for Patents
Washington, D.C. 20231

June 19, 2001

Dear Sir:

In response to the Notice to File Corrected Application Papers dated April 20, 2001 (copy attached), enclosed herewith for filing is a copy of the specification and drawings for the above-identified patent application. The specification and drawings have been corrected to meet 37 C.F.R. §1.52 and §1.84 requirements. No new matter has been added.

Though the Notice to File Corrected Application papers failed to indicate that any fee was due (as the appropriate filing fee was paid upon filing of the application), in the event that any additional fees are due with this paper, please charge Deposit Account No. 01-2340.

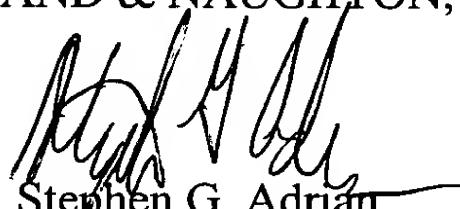
Applicants: **Norio FUKASAWA et al.**
Serial No.: **09/766,656**

Docket No.: **980233B**

In the event that this paper is not timely filed Applicants hereby petition for an appropriate extension of time. The fees for any such extension may be charged to our Deposit Account No. 01-2340. A duplicate copy of this paper is enclosed.

Respectfully submitted,

ARMSTRONG, WESTERMAN, HATTORI,
McLELAND & NAUGHTON, LLP



Stephen G. Adrian
Attorney for Applicant(s)
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Enclosures: Specification (254 pages)
Drawings (131 pages)